



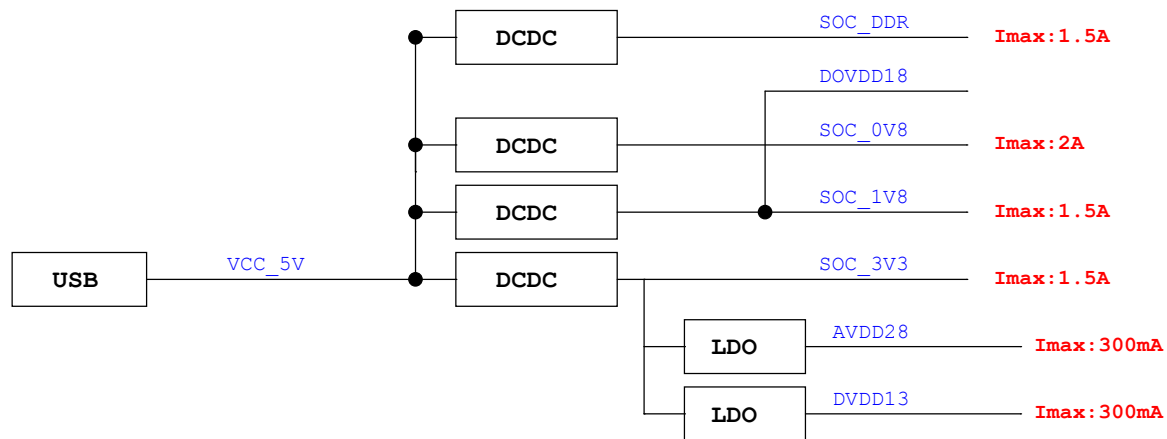
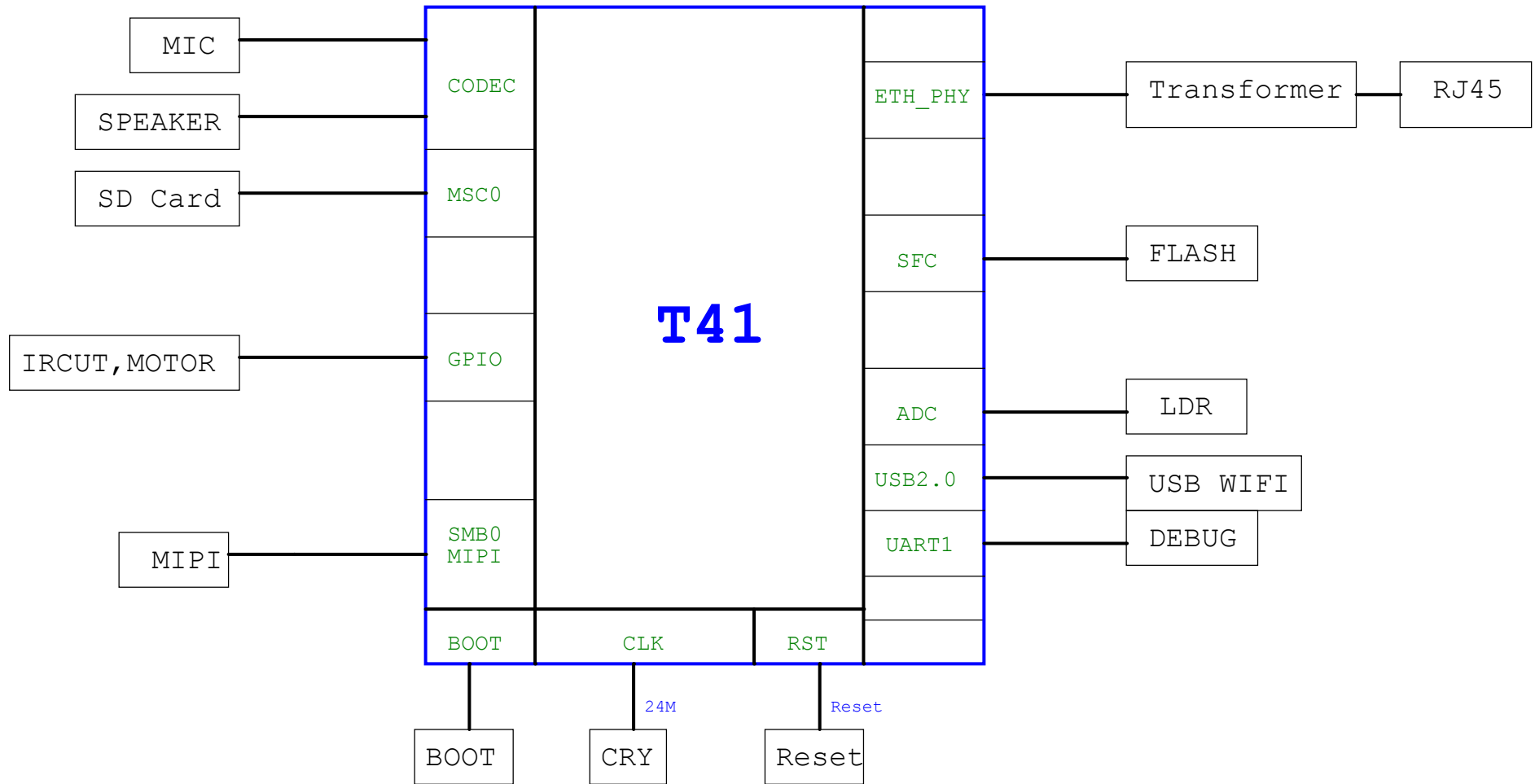
MARK_C90_MAIN

Schematic Revision 1.2

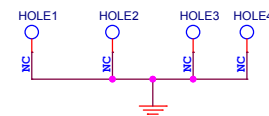
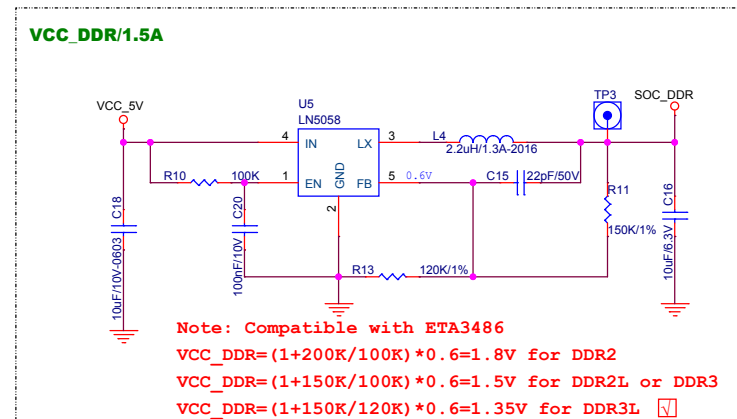
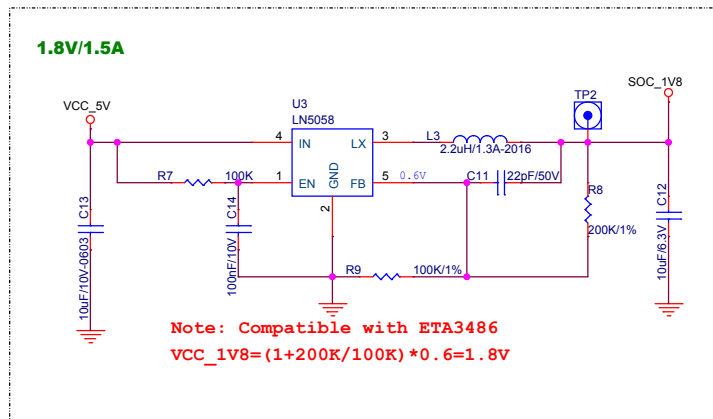
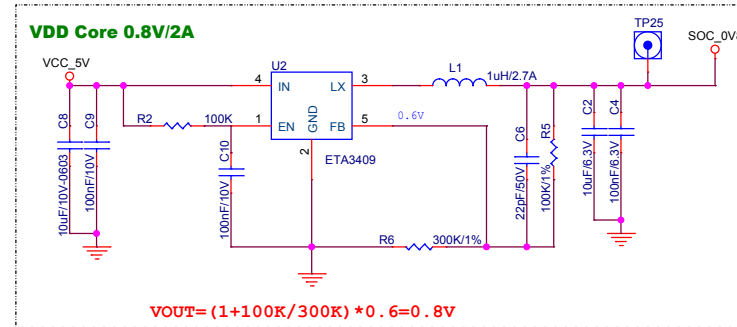
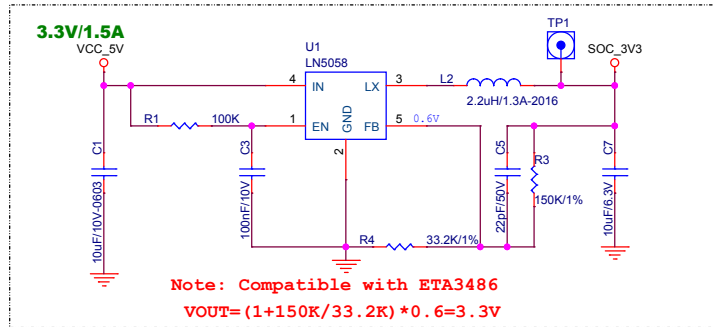
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<Core Design>

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POWER-SUPPLY



Mechanical Hole

<Core Design>

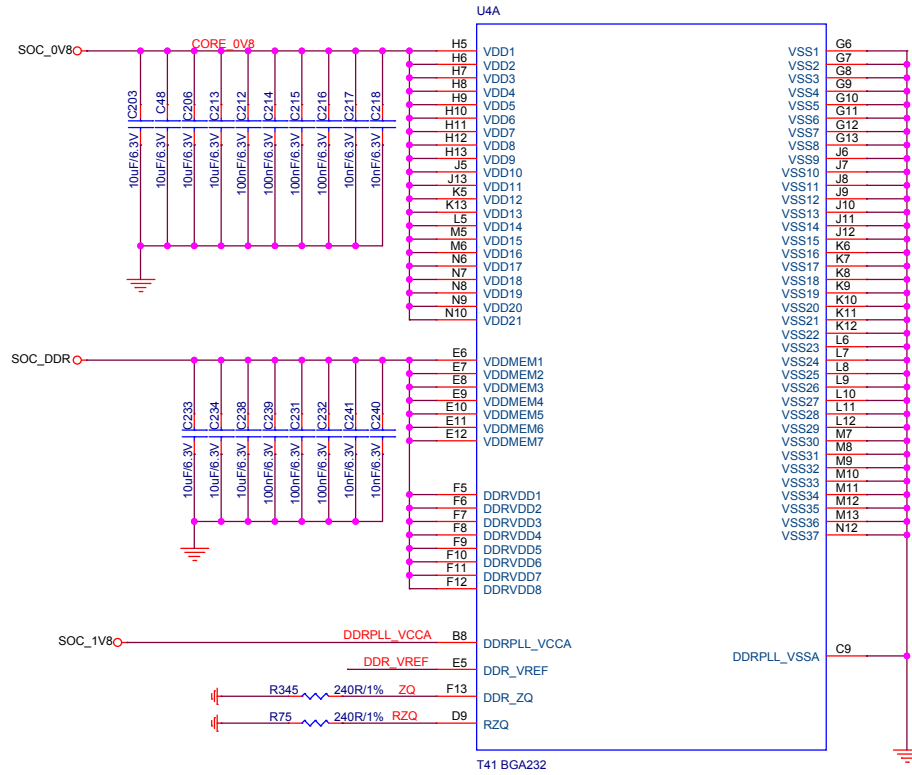
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T41_POWER



<Core Design>

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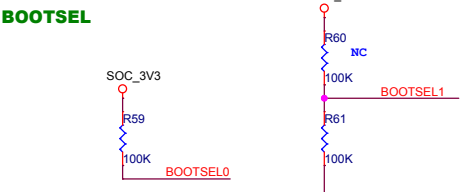
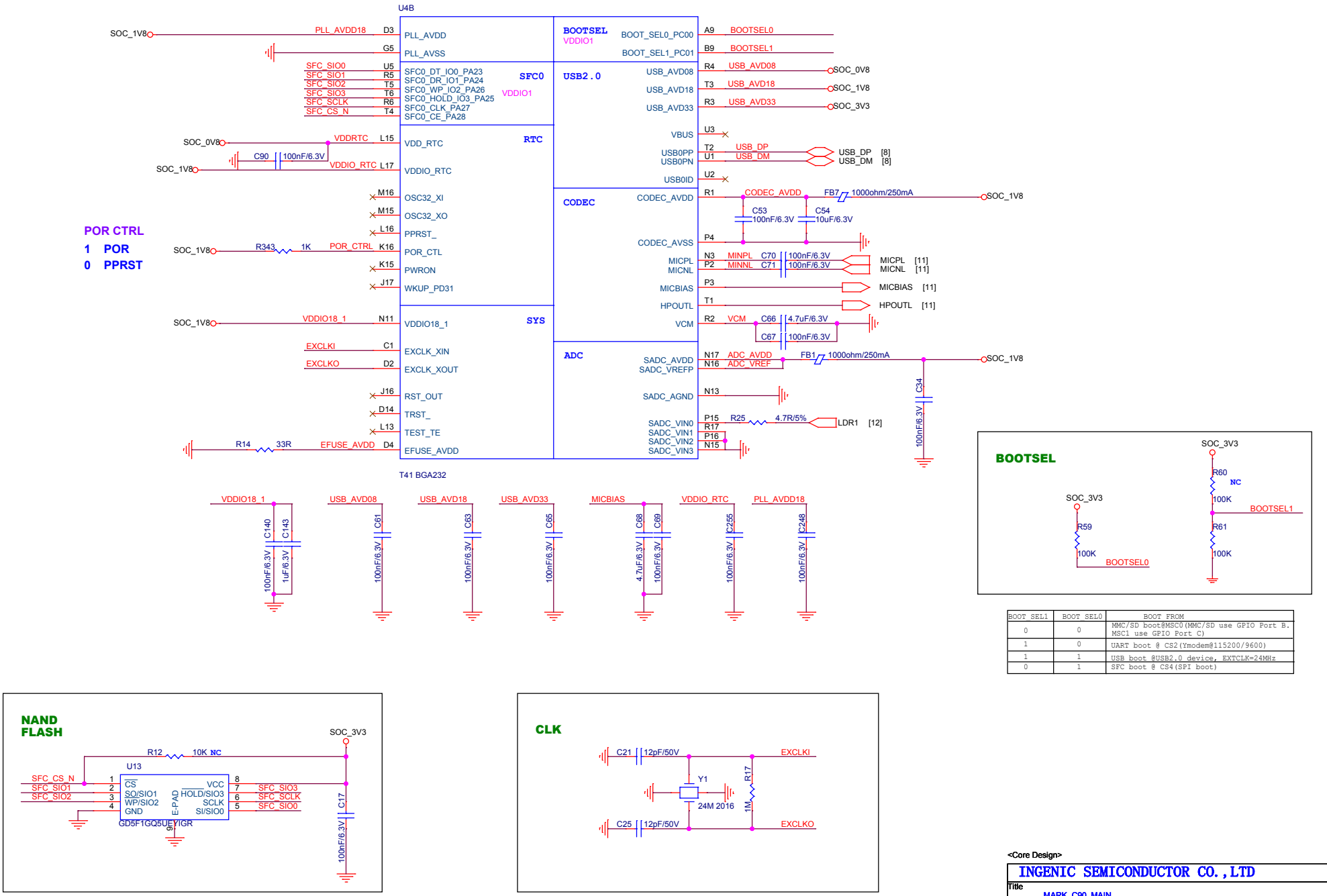
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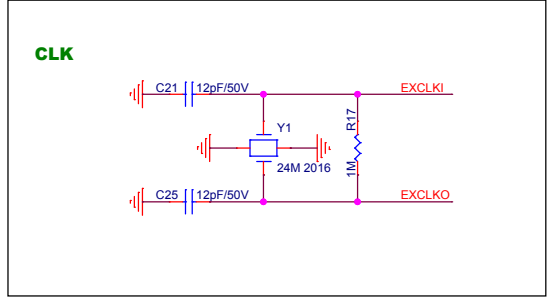
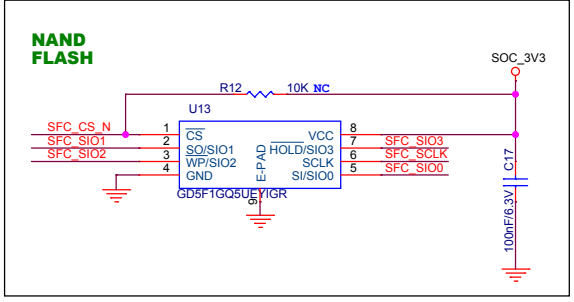
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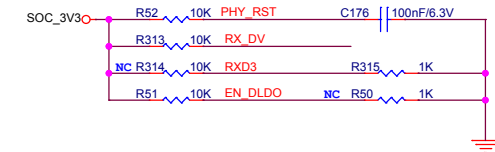
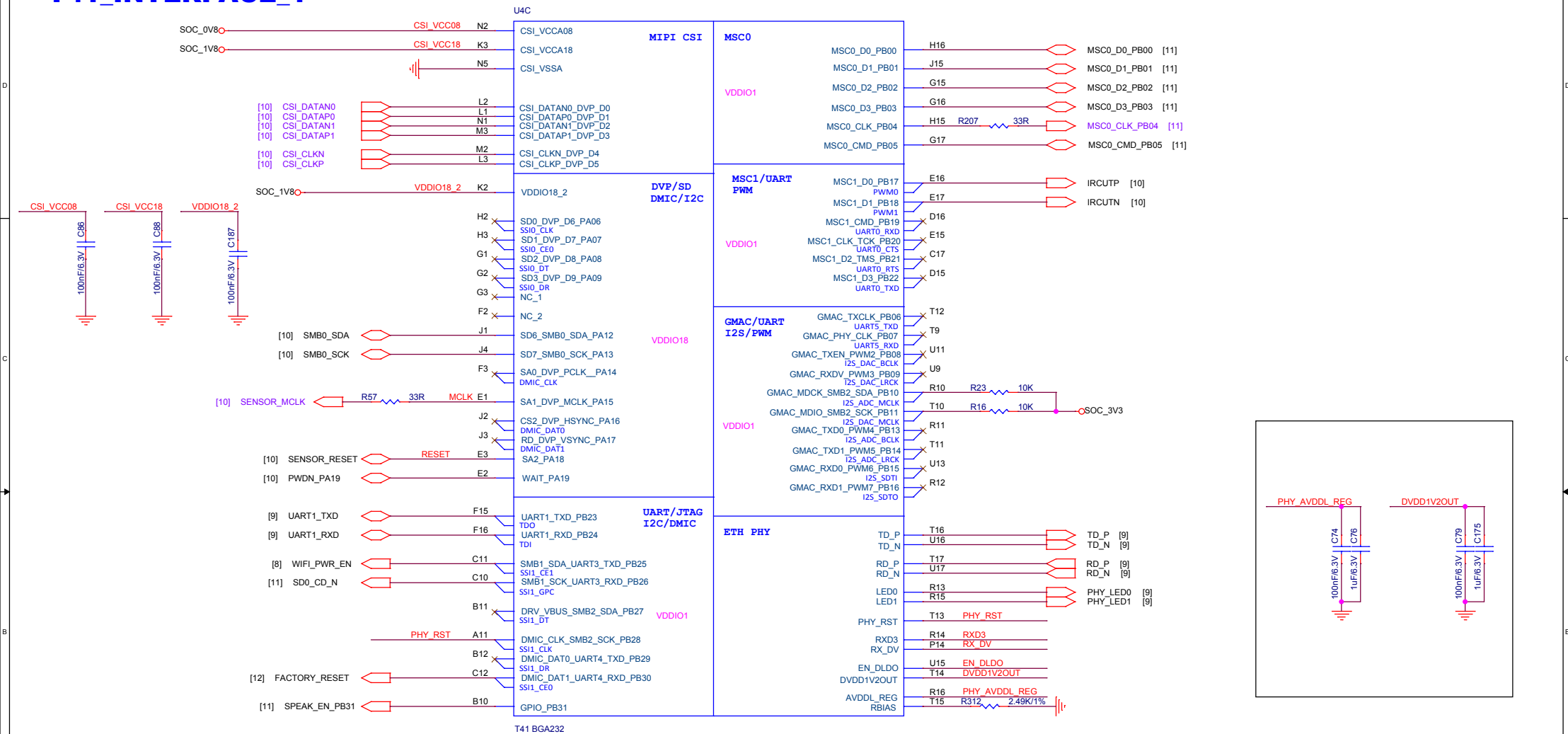
T41_SYS_APeripheral



BOOT SEL1	BOOT SEL0	BOOT FROM
0	0	MMC/SD boot (MMC/SD use GPIO Port B, MSCI use GPIO Port C)
1	0	UART boot @ CS2 (Ymodem@115200/9600)
1	1	USB boot (USB2.0 device, EXTCCLK=24MHz)
0	1	SFC boot @ CS4 (SFI boot)



T41_INTERFACE_1

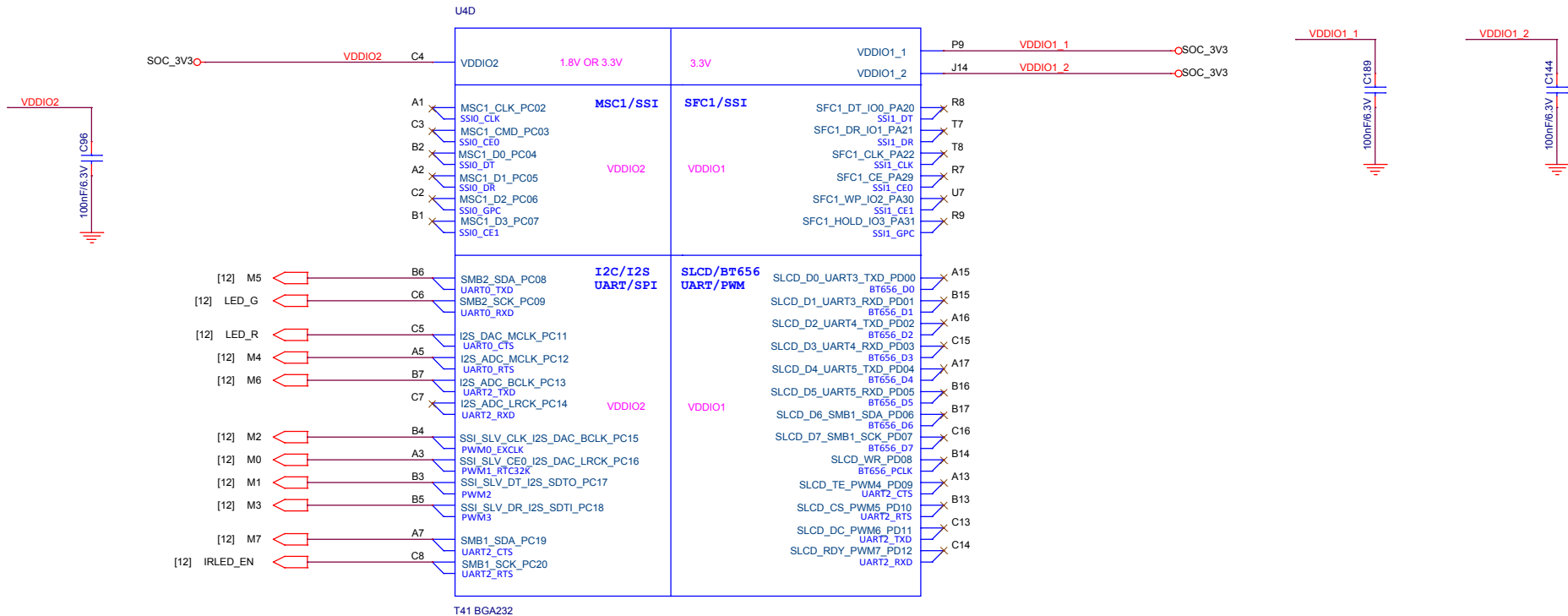


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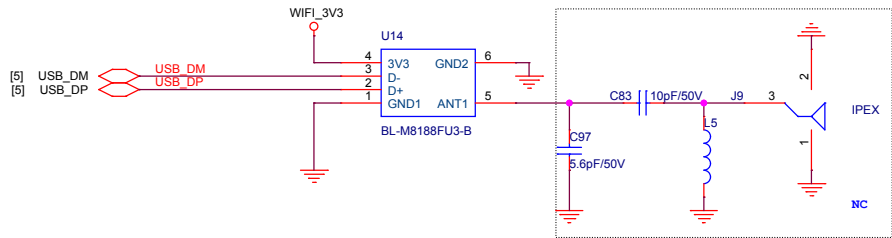
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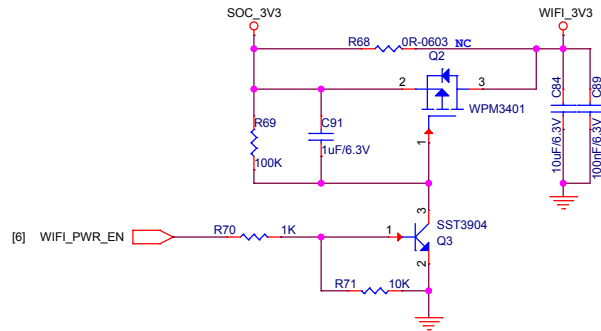
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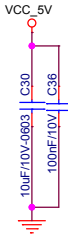
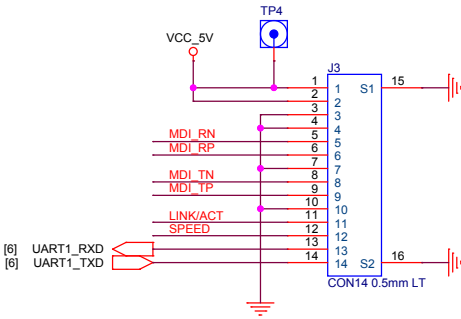
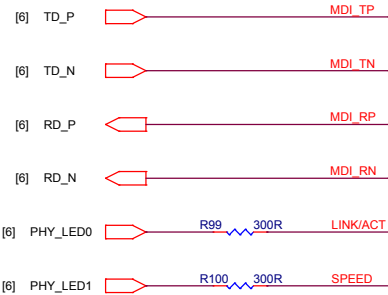
USB WIFI



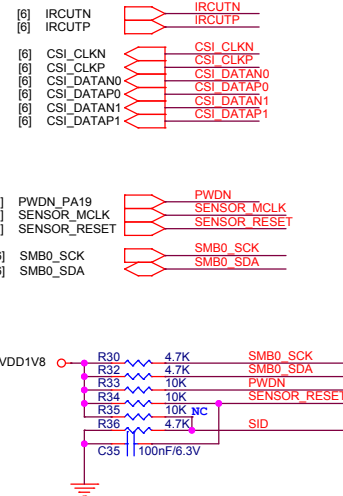
Supported other wifi module: BL-M8188FU3
BL-M8188EU1
BL-7601MU2



ETH_PHY



CIS



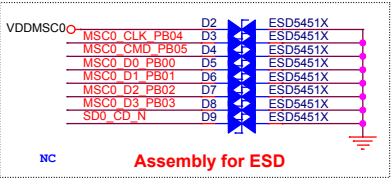
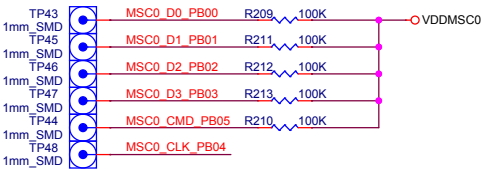
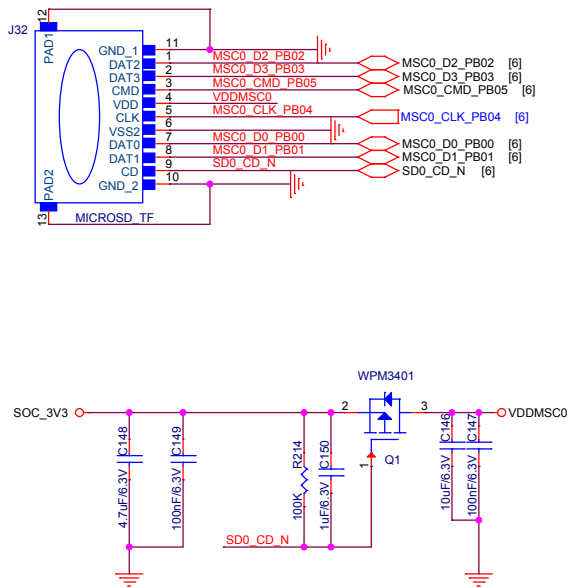
IRCUT



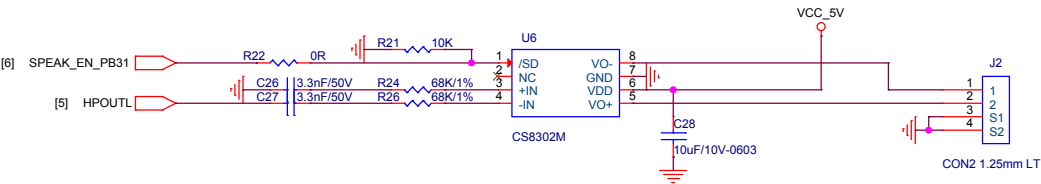
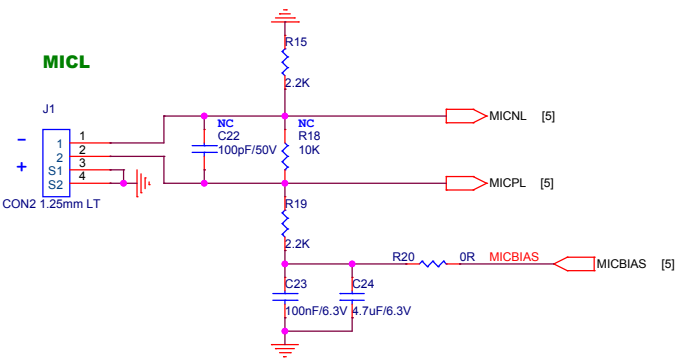
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SD_AUDIO

SD CARD

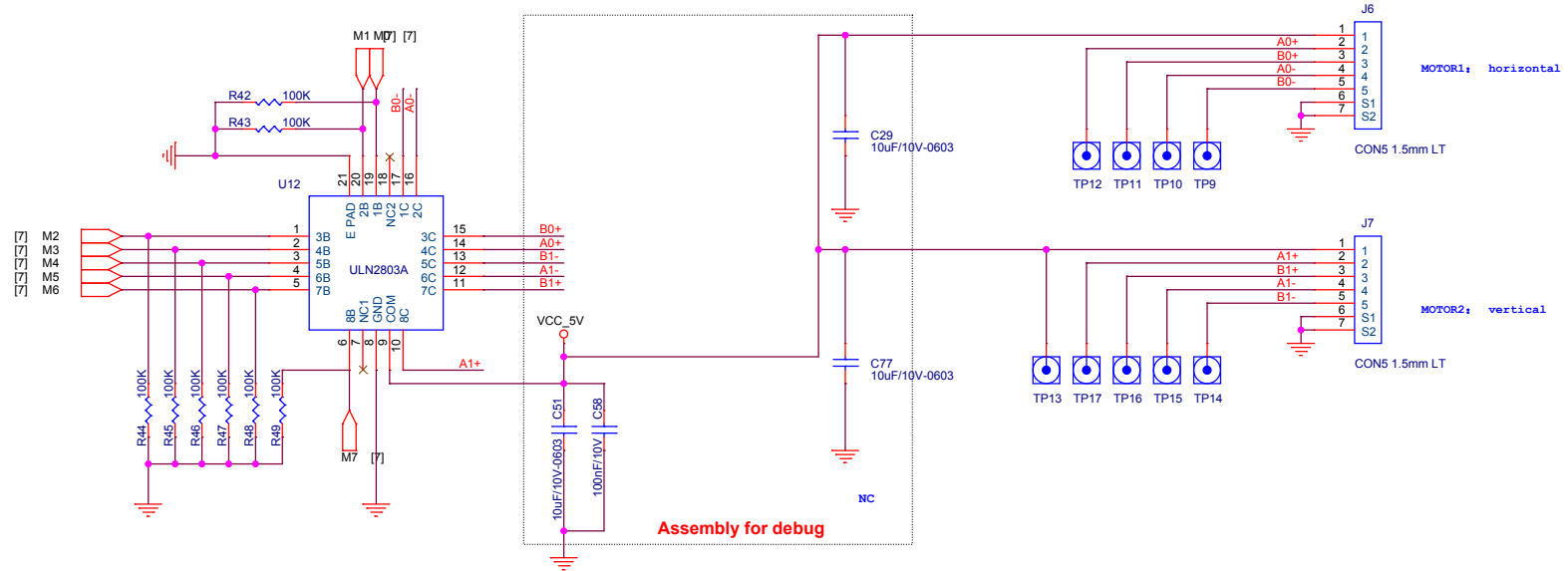


MICL

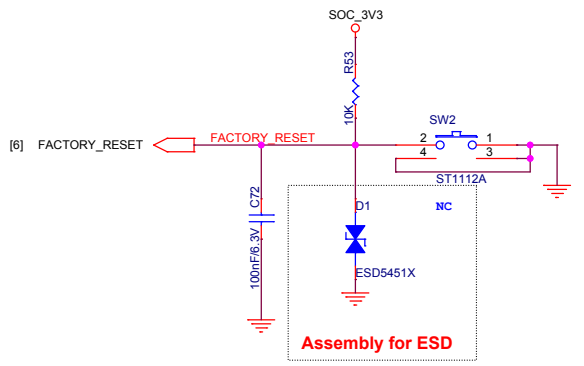
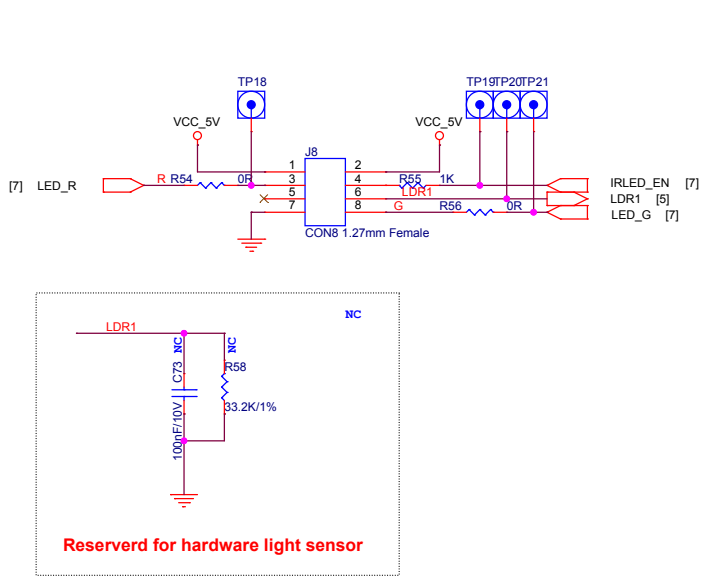


MOTOR_RST_LED

MOTOR



LED



Date	Revision	Change
July 01 2022	Rev1.0	1. First Revision
Sep 20 2022	Rev1.1	1. PHY_RST Changed From PB19 To PB28. 2. SYS_RESET Changed From PC14 To PB30.
Nov 01 2022	Rev1.2	1.T41.G3&F2 Ball Change to NC. 2.ADC Channel Series 4.7R Resistor. 3.Update T41 package